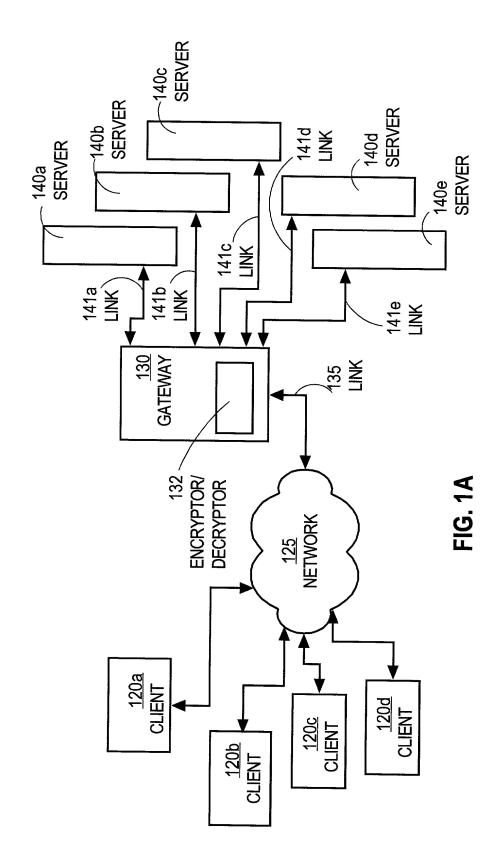
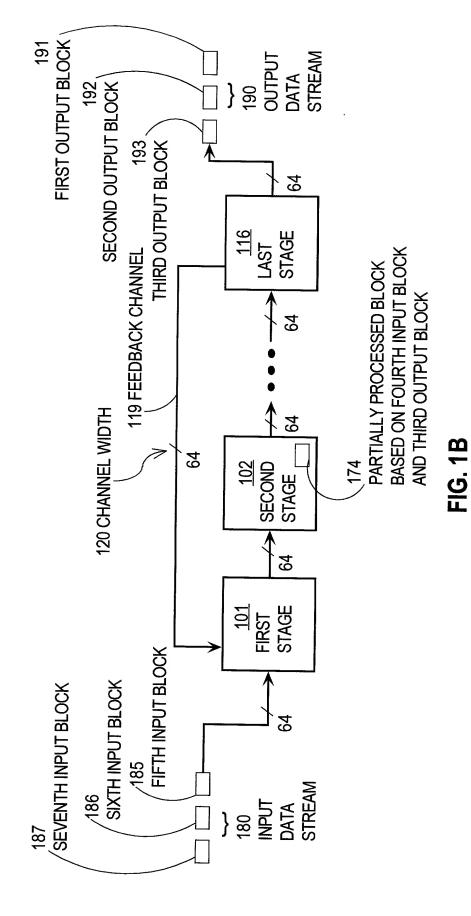
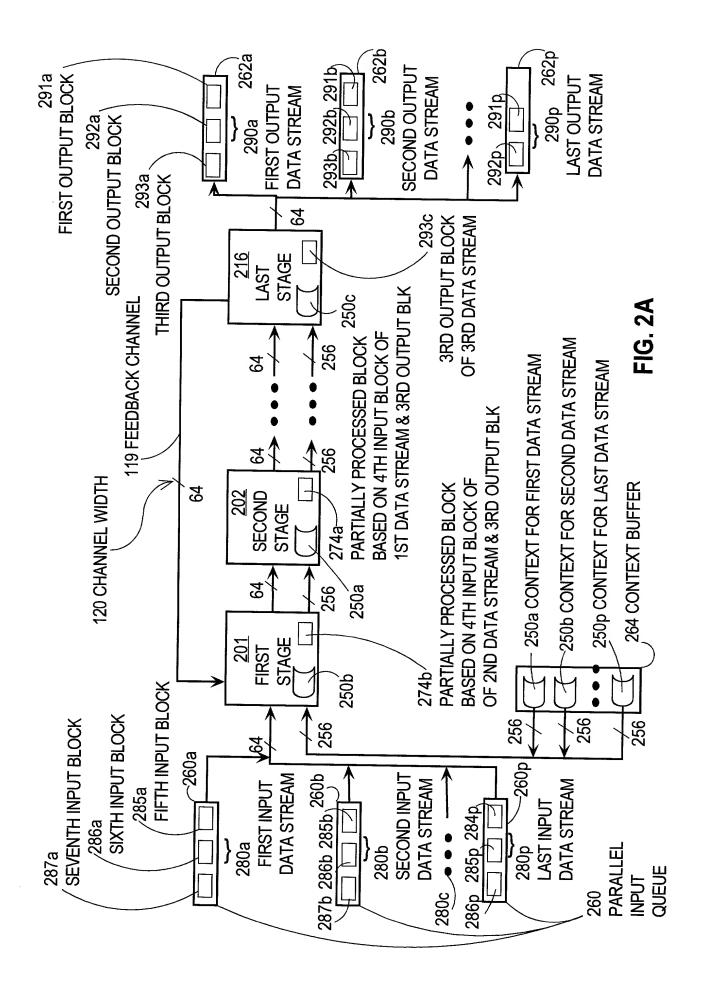
909

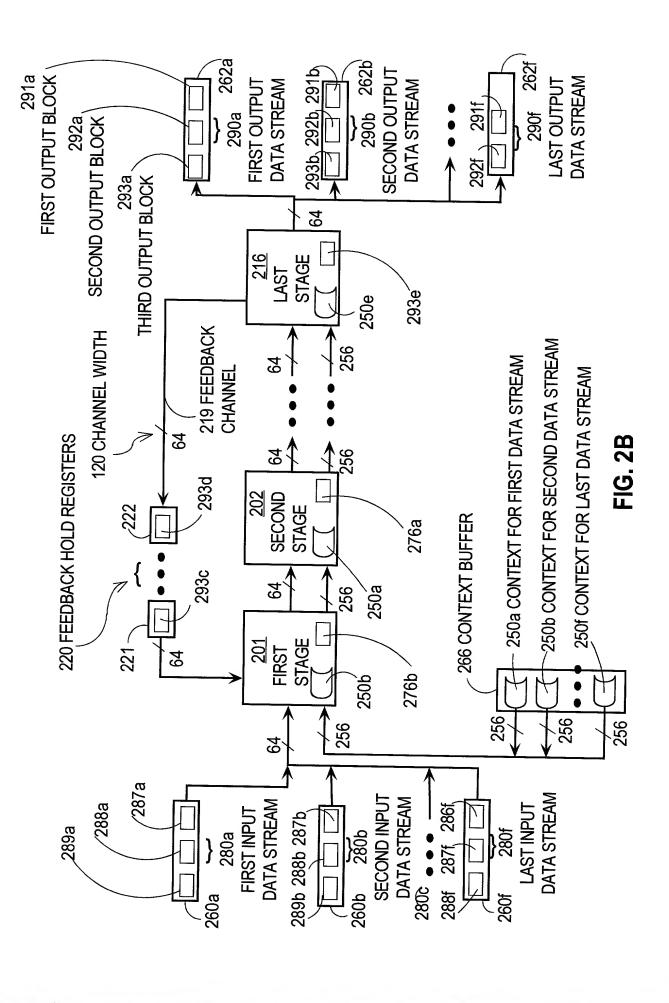


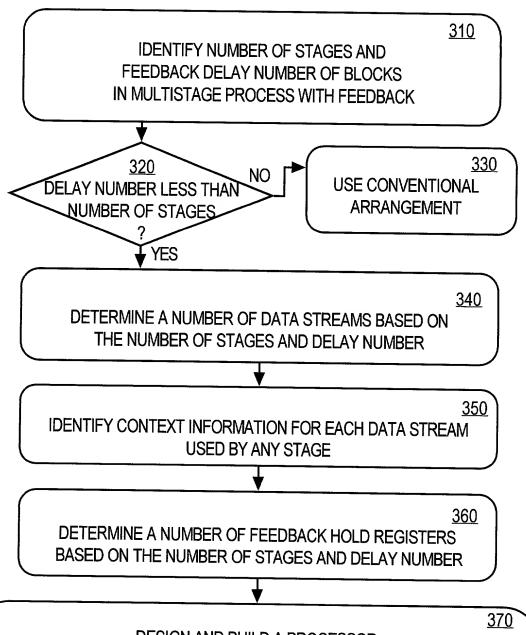
and the other Hillers or an afteres or



(PRIOR ART)







DESIGN AND BUILD A PROCESSOR
TO ALTERNATELY DIRECT THE NUMBER OF DATA STREAMS
AND CORRESPONDING CONTEXT INFORMATION TO THE FIRST STAGE;
TO PROCESS THIS INFORMATION IN SERIES THROUGH ALL STAGES; AND
TO DIRECT OUTPUT FROM THE FINAL STAGE
THROUGH THE FEEDBACK HOLD REGISTERS TO THE FIRST STAGE.

FIG. 3

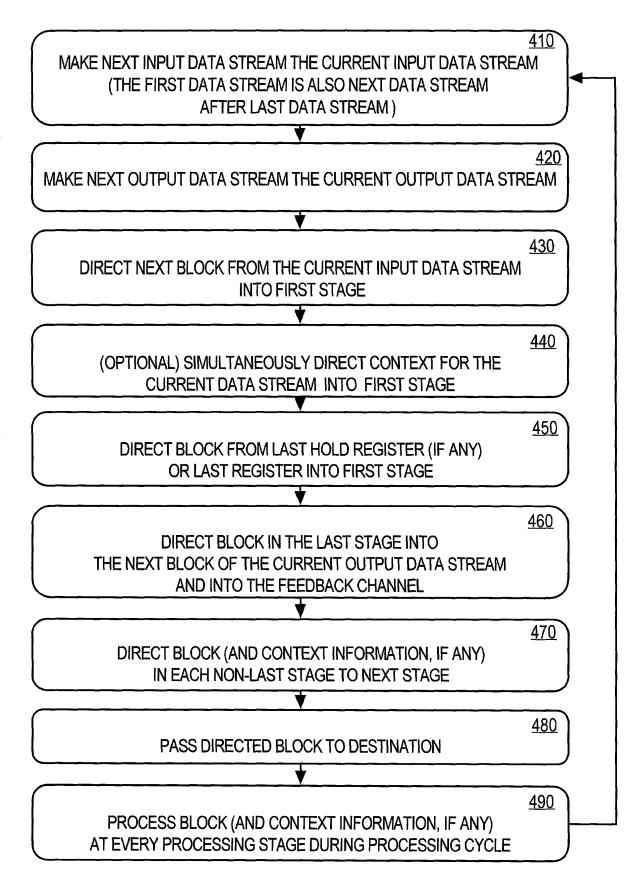


FIG. 4

FIG. 4

